

68. (New) The identification tag of claim 28, wherein the integrated circuit contains circuits for logic, sequencing and switching.

REMARKS

Applicant has elected to cancel all pending claims and present new claims 28 - 68 for examination. These new claims differ from the prior art now being relied upon by the Examiner because the claims now specify two separate dipole antennas, one for receiving and one for transmitting (new claim 27) or a single dipole used for receiving and transmitting (new claim 28).

Applicant has reviewed the prior art now being asserted and has found that this prior art does not include dipole antennas. Instead, antennas shown and claimed in these patents are thin, spiral trace of antennas around the perimeter of the chip. In contrast, Applicant's antennas are two rectangular areas connected at the center as shown in Applicant's photograph of the chip. Applicant's technology does not utilize a thin, metal trace spiralling around the chip.

Attached hereto is a marked-up version of the changes made to the specification and claims by the current amendment. The attached page is captioned "Version with Marking to Show Changes Made."

In view of the foregoing, it is respectfully submitted that the application is now in condition for allowance, and early action

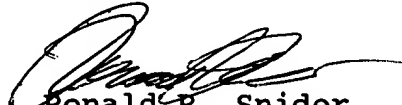
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in accordance thereof is requested. In the event there is any reason why the application cannot be allowed in this current condition, it is respectfully requested that the Examiner contact the undersigned at the number listed below to resolve any problems by Interview or Examiner's Amendment.

Respectfully submitted,


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Date: July 12, 2001

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

In the Claims:

Claims 1, 3 - 22, and 24 - 26 have been canceled.

New claims 27 - 68. have been added.